



Jeff Amelang

Jeff is a PhD student at the California Institute of Technology. He is an expert in HPC, having worked at Caltech on atomistic MD and at Lawrence Livermore National Laboratory on load balancing of very large simulations

Caltech Invited Speaker Lecture Series on HPC (High Performance Computing)

engineering. These computers are these techniques. programmed for speed of execution and the programs written for them are written specifically to The lecture series is an introduction to the world of use these computers optimally. Learning the basics HPC and the techniques used in this field, and is

High Performance Computing interested in doing research in scientific computing. (HPC) is the cutting edge of The techniques learned in HPC are also very useful computational capability, as the hardware innovations that drive the cutting where the largest and fastest edge of computing today are the technologies of computers in the world are mainstream computing tomorrow (massively parallel used to solve problems in processing, GPGPU, etc). Any programmer who physics, chemistry, biology and writes calculation-intensive code benefits from

of HPC is essential for any programmer that is open to all Computer Science and Science Majors.

LOCATION&TIME	MARCH 25 MON	MARCH 26 TUE	MARCH 27 WED	MARCH 28 THU
MATHEMATICUM LECTURE ROOM E 17:00-20:00	Single Core: cache awareness, data locality, boundedness, vectorization	Threading: introduction, pthreads, openmp, intel threading building blocks	Parallelization paradigms, clusters, communication, load balancing	Coprocessors (GPUs, intel phi) software tools, data structures